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Letter of Participation

Date of Issue: 26-06-2025

This certificate is awarded to **Ritesh Suresh Ashtkar** from **National Institute of Technology Karnataka, Surathkal** for attending **Technical Session on “FPGA for beginners”** conducted part of the **e-Yantra Robotics Competition (eYRC)**. The online session was conducted on 26th June 2025 and it covered following topics:

1. Introduction of FPGA
2. Architecture of FPGA
3. Basics of Verilog HDL
4. Hands-on Activity on FPGA

He/She has scored **14** marks out of **15** in the quiz conducted during the session.

Prof. Shivaram Kalyanakrishnan
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