IIT Bombay

Attendance Certificate

Date of Issue: August 12th, 2024

This certificate is awarded to **Jayesh Karemore** for attending the Technical Session on **"FPGA"** conducted part of the **e-Yantra Robotics Competition (eYRC)**. The online session was conducted on **August 12th, 2024** and it covered following topics:

1. Electronic Design
2. Evolution of FPGA
3. FPGA, ASIC, uController
4. Introduction to HDL
5. Scope of FPGA

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Prof. Kavi Arya
Principal Investigator, e-Yantra
Professor
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Indian Institute of Technology Bombay

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