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## Attendance Certificate

**Date of Issue: August 20th, 2022**

This certificate is awarded to **Bhadoriya Aryan** for attending “**Introduction to FPGA design**” conducted through the **e-Yantra Lab Setup Initiative (eLSI)**. The session was conducted on August 20th, 2022 and it covered following topics:

1. Introduction to FPGAs
2. FPGA vs uP vs ASIC
3. Verilog HDL basics
4. ALU design and simulation

Prof. Kavi Arya  
Principal Investigator, e-Yantra  
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