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## Attendance Certificate

**Date of Issue: December 23rd, 2021**

This certificate is awarded to **Ujeniya Pushti Bharatbhai** for attending “**Technical Session on Digital VLSI Design and FPGA Implementation**” conducted through the **e-Yantra Lab Setup Initiative (eLSI)**. The session was conducted on December 24th, 2021 and it covered following topics:

1. VLSI fundamentals
2. What is Programmable Logical Stuff?
3. Overview : HDL based Design
4. CAD tools
5. FPGA Implementation

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Prof. Kavi Arya  
Principal Investigator, e-Yantra  
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