



Department of Computer Science & Engg.  
Kanwal Rekhi Building  
Indian Institute of Technology Bombay  
Powai, Mumbai 400076 INDIA  
Tel. : +91 22 25764986  
email : support@e-yantra.org



IIT Bombay

## Attendance Certificate

**Date of Issue: December 23rd, 2021**

This certificate is awarded to **Ujeniya Pushti Bharatbhai** for attending “**Technical Session on Digital VLSI Design and FPGA Implementation**” conducted through the **e-Yantra Lab Setup Initiative (eLSI)**. The session was conducted on December 24th, 2021 and it covered following topics:

1. VLSI fundamentals
2. What is Programmable Logical Stuff?
3. Overview : HDL based Design
4. CAD tools
5. FPGA Implementation

Prof. Shivaram Kalyan Krishnan  
Principal Investigator, e-Yantra  
Associate Professor  
Department of Computer Science and Engineering  
Indian Institute of Technology Bombay



1c43eadcbe0c6e782623eb7722bceb917e96a4cb